

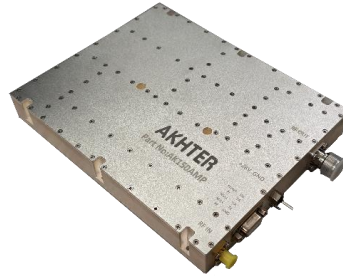


Microwave 50W-Solid State Broadband HPA (AK206050)

50W-Solid State Broadband HPA (AK206050)

◆ Product Feature

- Ultra-wide bandwidth
- High reliability
- Easy to maintain



◆ Product Specifications

Item	Specification	Remarks
Frequency Range	2000MHz ~6000MHz	
Pin(dBm)	-9dBm~-6dBm	
Pout(dBm)	(47±1) dBm	Test Wave: CW
Gain, Small signal	61db	Test Wave: CW
Gain adjustment range	15dB	
Gain Adjustment step	0.5db	
Maximum input power without damage	10dBm	
Gain flatness	±3dB	
Input return loss	≤-10dB	
Noise figure	≤20dB	
Harmonics	≤-10dBC@Odd Harmonics; ≤-12dBC@Even Harmonics	
Spurious	≤-60dBc	
PA Enable / Disable time	≤1uS	
Working Voltage	DC 28V±1V	
Maximum Working Current	9A	
RF In connector	SMA-Female	
RF Out connector	N-Female	
Power Supply Connector	Pull-core Capacitance	
Working Temperature	-40~+65°C	
Storage Temperature	-55~+85°C	
Maximum Altitude	3000m	MIL-STD-810F Method 500.4
Vibration/Shock	Airborne	MIL-STD-810F Method 514.5/516.5 – Proc 1
Protection & Alarm	Over-temperature 75°C and Over-VSWR	
Size	200mm *158mm *25mm	Exclusive Connector
Weight	≤1.4Kg	
Cooling	External Heatsink Required	

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◆ DC INTERFACE CONNECTOR

Pull-core Capacitance	A1	Ground Return	
Pull-core Capacitance	A2	+24.0-32.0VDC, 28.0VDC Nominal	Supply Voltage:
Monitoring Interface (DB15, Female)	PIN1	Not Used	
	PIN2	Temperature Reporting	Analog Output Voltage @ 10mV/°C with a 500mV offset (i.e. 0.75V = 25°C)
	PIN3	Not Used	
	PIN4	Not Used	
	PIN5	Attenuator Setting	Voltage input in the range of 0.5-3.0VDC, 0.5VDC corresponds with minimum attenuation, 3.0VDC is maximum attenuation.
	PIN6	PA Enable	0/3.3V logic levels: Power Amplifier disable is a TTL Logic Low (0V). (Internally Pulled-High 3.3V)
	PIN7	Alarm	Amplifier Alarm indicator: Normally TTL Low A logic High indicates a fault condition, 0/3.3V Logic Levels
	PIN8	Not Used	
	PIN9	Current Reporting	Analog output voltage range of 1V/10A (i.e. 1.5V = 15A)
	PIN10	Not Used	
	PIN11	Not Used	
	PIN12	Not Used	
	PIN13	Not Used	No Connection
	PIN14	Not Used	No Connection
	PIN15	Reset	Hardware reset Logic 0 to reset PA and clear latched faults

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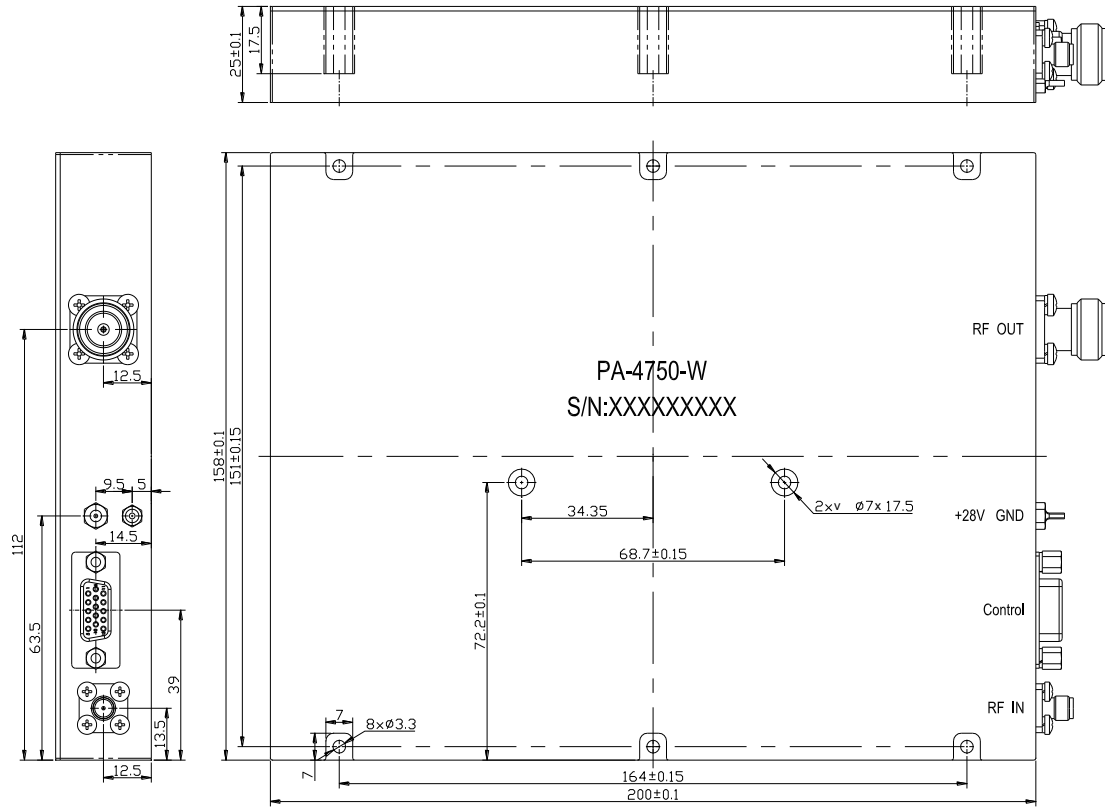
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◆ Product Outline Dimension Drawing



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